

GR 98 P 2078



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By: Law D. Pearson

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Jens Barrenscheen et al.
Appl. No. : 09/124,288
Filed : July 28, 1998
Title : Memory Device And Method For Operating The
Memory Device
Examiner : Patrice Winder
Group Art Unit : 2758

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A M E N D M E N T

Hon. Commissioner of Patents and Trademarks
Washington, DC 20231

S i r :

Responsive to the Office action dated August 12, 2000, kindly
amend the above-identified application as follows:

In the Specification:

At page 7, lines 17-18, please replace "Fig. 3 is a schematic
illustrating four different operating modes for operating the

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